



Dkt. 2271/71530

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Toshiki YAMANAKA

Serial No.: 10/776,481

Group Art Unit: 2824

Date Filed: February 10, 2004

Examiner: Anh K. Phung

For: SEMICONDUCTOR MEMORY DEVICE

Conf. No.: 5995

Class-Subclass: 365-189050

Issue Fee Due Date: June 23, 2005

Mail Stop Issue Fee

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**APPLICANT'S COMMENTS ON
EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE**

Sir:

Applicant appreciates the Examiner's Statement of the Reasons for Allowance attached to the Notice of Allowability dated March 23, 2005 and submits that the allowed claims recite subject matter which further supports patentability for reasons in addition to those identified in the Reasons for Allowance.

Respectfully submitted,

PAUL TENG, Reg. No. 40,837
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I hereby certify that this correspondence is being deposited this date with the U.S. Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Paul Teng
Reg. No. 40,837

June 22, 2005
Date



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Sir:

COMMUNICATION FORWARDING CORRECTED FORMAL DRAWINGS

Applicant submits herewith Figs. 12-15 in connection with the above-identified application. The enclosed formal drawings are submitted in response to the Notice of Allowability dated March 23, 2005, in which the Examiner required Applicants to submit a new set of corrected drawings for Figs. 12-15 labeled as "PRIOR ART". A response to the March 23, 2005 Notice of Allowability is due June 23, 2005. Accordingly, this Communication is being timely filed.

No fee is deemed necessary in connection with the filing of this Communication. However, if any fee is required, authorization is hereby given to charge the amount of such fee to Deposit Account No. 03-3125.

Respectfully submitted,

Paul Teng, Reg. No. 40,837
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